



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Attorney Docket Nos. 107262.199US1 and AMAT7090/P01)

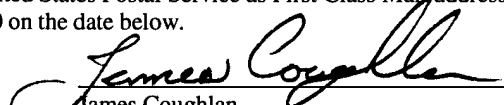
In re Application of: West et al.) Examiner: To Be Assigned
Serial No. 10/623,665) Group Art Unit: To Be Assigned
Filing Date: July 21, 2003)

For: OPTICAL READY WAFERS

CERTIFICATE OF FIRST CLASS MAILING UNDER 37 CFR §1.8

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Date: 11-03-03


James Coughlan

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

Dear Sir:

Enclosed for filing in the above-identified patent application are the following documents:

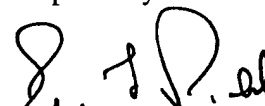
1. Information Disclosure Statement (1 page);
2. Forms PTO-1449 (4 Pages) Copied From Parent Application (10/280,492);
3. Certificate of First Class Mailing; and
4. Return Postcard.

No fees are believed to be due in connection with this submission. However, please charge any fees that might be due or credit any overpayment to our Deposit Account No. 08-0219.

If there are any questions, please call the undersigned at the number below.

Respectfully submitted,

Date: November 3, 2003
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Eric L. Prah, Reg. No. 32,590
Attorney for Applicants



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
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James Coughlan

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.97(b) and 1.98(d) Applicants submit herewith the attached Form PTO-1449 for filing in the above-referenced application. The present application is a continuation-in-part of U.S. Patent Application No. 10/280,492. The Form PTO-1449 attached herewith lists references that were already submitted by the Applicants in the parent application. Therefore, pursuant to 37 C.F.R. § 1.98(d), no copies of the previously cited art are enclosed. This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) before the mailing date of the first Office Action, therefore no fee is believed to be due.

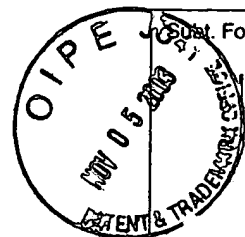
Please charge any fees that might be due in connection with this matter to Deposit Account No. 08-0219. If there are any questions, please call the undersigned at the telephone number indicated below.

Respectfully submitted,



Eric L. Prah, Reg. No. 32,590
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Sub. For, PTO-1449

INFORMATION DISCLOSURE
IN AN APPLICATION

(Use several sheets if necessary)

COPY

Docket Number

107262.176 US2

Application Number

10/280,492

Applicant

West et al.

Filing Date

October 25, 2002

Group Art Unit

2812

Sheet

1

OF

4

U.S. Patent Documents

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,217,856	Nov 16, 1965	G.T. Miles	197	127	Mar 28, 1963
	4,006,432	Feb 1, 1977	Streifer et al.	331	94.5 C	Oct 15, 1974
	4,100,562	Jul 11, 1978	Sugawara et al.	357	19	Apr 19, 1976
	4,368,481	Jan 11, 1983	Ohashi et al.	357	30	Jun 10, 1980
	4,438,447	Mar 20, 1984	Copeland, III et al.	357	19	Jan 18, 1982
	4,720,468	Jan 19, 1988	Menigaux et al.	437	129	Mar 29, 1985
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	4,759,023	Jul 19, 1988	Yamaguchi	372	26	Jan 6, 1986
	4,959,540	Sep 25, 1990	Fan et al.	250	227.12	May 15, 1989
	4,980,568	Dec 25, 1990	Merrick et al.	250	551	May 22, 1989
	5,190,883	Mar 2, 1993	Menigaux et al.	437	3	May 16, 1991
	5,291,010	Mar 1, 1994	Tsuji	250	208.1	Oct 3, 1991
	5,193,131	Mar 9, 1993	Bruno	385	14	Apr 28, 1992
	5,298,787	Mar 29, 1994	Bozler et al.	257	592	Apr 1, 1991
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	5,523,557	Jun 4, 1996	Bruno	250	214.1	Apr 26, 1995
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	5,548,433	Aug 20, 1996	Smith	359	158	Apr 26, 1993
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	5,801,872	Sep 1, 1998	Tsuji	359	248	Feb 7, 1996

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP § 609: Draw Line through citation if not conformance and not considered. Include copy with next communication to applicant.

Subt. For, PTO-1449

INFORMATION DISCLOSURE
IN AN APPLICATION

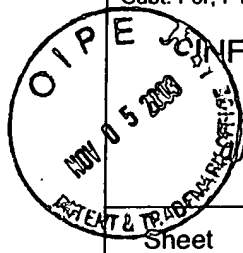
(Use several sheets if necessary)

2 OF **4**

Docket Number 107262.176 US2	Application Number 10/280,492
Applicant West et al.	
Filing Date October 25, 2002	Group Art Unit 2812

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	5,825,051	Oct 20, 1998	Bauer et al.	257	82	Sep 12, 1997
	5,828,679	Oct 27, 1998	Fisher	372	6	Jul 23, 1997
	5,854,804	Dec 29, 1998	Winer et al.	372	38	Dec 13, 1996
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EXAMINER	DATE CONSIDERED
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Subt. For, PTO-1449				Docket Number 107262.176 US2		Application Number 10/280,492	
INFORMATION DISCLOSURE IN AN APPLICATION <i>Use several sheets if necessary</i>				Applicant West et al.			
				Filing Date October 25, 2002		Group Art Unit 2812	
Sheet	3	OF	4				

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	2002/0174826 A1	Nov 28, 2002	Maydan et al.	117	84	May 24, 2001
	2002/0174827 A1	Nov 28, 2002	Samoilov et al.	117	84	Dec 11, 2001
	2003/0015720 A1	Jan 23, 2003	Lian et al.	257	98	Jul 18, 2001
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	2003/0072548 A1	Apr 17, 2003	Bhardwaj et al.	385	129	Oct 12, 2001
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Foreign Patent Documents

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	WO 03/036369 A1	May 1, 2003	PCT				

Other Documents (Including Author, Title, Date, Pertinent Pages, Etc.)

A	Chen et al., "Fully Embedded Board-Level Guided-Wave Optoelectronic Interconnects," Proceedings of the IEEE, Vol. 88, No. 6, June 2000, pp. 780-793
B	Choi et al., "Self-Aligning Silicon Groove Technology Platform for the Low Cost Optical Module," 1999 Electronic Components and Technology Conference, IEEE, 1999, pp. 1140-1144
C	Irace et al., "Fast Silicon-on-Silicon Optoelectronic Router Based on a BMFET Device," IEEE Journal of Selected Topics in Quantum Electronics, Vol. 6, No. 1, January/February 2000, pp. 14-18
D	Jalali et al., "Advances in Silicon-on-Insulator Optoelectronics," IEEE Journal of Selected Topics in Quantum Electronics, Vol. 4, No. 6, November/December 1998, pp. 938-947

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INFORMATION DISCLOSURE IN AN APPLICATION (Use several sheets if necessary)				Applicant West et al.			
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Sheet	4	OF	4				

E	Kimerling, "Photons to the Rescue: Microelectronics Becomes Microphotonics," The Electrochemical Society <i>Interface</i> , Summer 2000, pp. 28-31
F	Kostal, "Nano-optics Changes the Rules for Optical Components," NanoOpto White Paper 2002.001, June 2002, pp. 1-6
G	Kostal and Park, "Nano-optics: New Rules for Optical System Design," NanoOpto White Paper 2002.002, June 2002, pp. 1-10
H	Motorola Labs, "The Discovery: Motorola Labs solves a 30-year semiconductor industry puzzle," June 2002
I	Schaub et al., "High-Speed Monolithic Silicon Photoreceivers on High Resistivity and SOI Substrates," Journal of Lightwave Technology, Vol. 19, No. 2, February 2001, pp. 272-278
J	Yabu et al., "New Design Method for Low-Loss Y-Branch Waveguides," Journal of Lightwave Technology, Vol. 19, No. 9, September 2001, pp. 1376-1384
K	Tashiro et al., "A Selective Epitaxial SiGe/Si Planar Photodetector for Si-Based OEIC's," IEEE Transactions on Electronic Devices, Vol. 44, No. 4, April 1997, pp. 545-550
L	Walker and Jahns, "Optical clock distribution using integrated free-space optics," Optics Communications 90 (1992) pp. 359-371
M	Walker et al., "Design and fabrication of high-efficiency beam splitters and beam deflectors for integrated planar micro-optic systems," Applied Optics, Vol. 32, No. 14, May 10, 1993, pp. 2494-2501

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